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| PTO-1449 (modified)<br><br>'List of Patents and Publications<br>For Applicant's Information<br>Disclosure Statement<br><br>(Use several sheets if necessary) | ATTY. DOCKET NO.<br>AMDI:133HON | SERIAL NO.<br>10/790,939   |
|  | APPLICANT<br>Akif Sultan et al. | CERTIFICATE OF MAILING 37 C.F.R. 1.8<br><br>I hereby certify that this correspondence is being deposited<br>with the U.S. Postal Service as First Class Mail in an envelope<br>addressed to: Commissioner for Patents, P.O. Box 1450,<br>Alexandria, Virginia 22313-1450 on the date below:<br><br>May 21, 2004<br>Date Timothy M. Honeycutt |
|  | FILING DATE<br>March 1, 2004    | GROUP  |

### U.S. PATENT DOCUMENTS

| EXAM<br>INIT. | REF.<br>DES. | DOCUMENT<br>NUMBER | DATE              | NAME                  | CLASS | SUB<br>CLASS | FILING DATE<br>IF APPROPRIATE |
|---------------|--------------|--------------------|-------------------|-----------------------|-------|--------------|-------------------------------|
| <i>m</i>      | A1           | 6,114,211          | September 5, 2000 | H. Jim Fulford et al. | 448   | 305          |                               |
| <i>h</i>      | A2           | 5,227,321          | July 13, 1993     | Ruoja Lee et al.      | 437   | 44           |                               |
| <i>h</i>      | A3           | 10/693,016         |                   | Scott Goad et al.     |       |              | October 24, 2003              |

### FOREIGN PATENT DOCUMENTS

| EXAM<br>INIT. | REF.<br>DES. | DOCUMENT<br>NUMBER | DATE | COUNTRY | CLASS | SUB<br>CLASS | TRANSLATION |
|---------------|--------------|--------------------|------|---------|-------|--------------|-------------|
|               |              |                    |      |         |       |              |             |

### OTHER ART

| EXAM<br>INIT. | REF.<br>DES. | AUTHOR; TITLE; JOURNAL; VOLUME; DATE; PERTINENT PAGES   |
|---------------|--------------|---|
| <i>h</i>      | A4           | Stanley Wolf and Richard N. Tauber; <i>Silicon Processing for the VLSI Era, Volume 3 - The Submicron MOSFET</i> ; pp.226-232, 240, 309-311 and 621-622; 1995  |
| <i>h</i>      | A5           | Terence B. Hook et al.; <i>High-Performance Logic and High-Gain Analog CMOS Transistors Formed by a Shadow-Mask Technique With a Single Implant Step</i> ; IEEE Transactions on Electron Devices, Vol. 49, No. 9, September 2002; pp. 1623-1627 |

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| EXAMINER <i>Quor Itany</i> | DATE CONSIDERED <i>02/2006</i> |
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement-PTO-1449 (Modified)